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PATENT NUMBER and
ISSUE DATE

U.S. UTILITY Patent Application

APPL NUM	FILING DATE	CLASS	SUBCLASS	GAU	EXAMINER
10002696	10/11/2001	138	2336	2336	H. WEISS

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**CONTINUING DATA VERIFIED:

None

*FOREIGN APPLICATIONS VERIFIED:

None

PG PUB DO NOT PUBLISH <input type="checkbox"/>	RESCIND <input type="checkbox"/>
Foreign priority claimed <input type="checkbox"/> yes <input checked="" type="checkbox"/> no	ATTORNEY DOCKET NO
35 USC 119 conditions met <input type="checkbox"/> yes <input checked="" type="checkbox"/> no	11587 M-12336 US
I have read and acknowledged Examiner's initials	
TITLE: Multi-state non-volatile integrated circuit memory systems that employ dielectric storage elements	

U.S. DEPT. OF COM. PAT. & TM. PROC. CH. REV. 12-94

EXAMINER'S MAIL		Assistant Examiner
SEARCHED	INDEXED	
CLASSIFIED		PREPARED FOR
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FILED WITH: ☐ DISK (CRF)

(Attached to patent application)